



In Place of FORM PTO-1449 (M-10-2000)

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANTS' INFORMATION DISCLOSURE  
STATEMENT**

Serial No.: 09/737,555  
Applicants: C. Moore et al.  
Filing Date: December 15, 2000  
Group: 2183  
Atty. Docket No.: AUS9-2000-0001-US1

#4

**Reference Designation**

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
____AAA						
____ABC						
____ACA						
____ADA						
____AEA						
____AFA						
____AGA						
____AHA						
____AIA						
____AJA						
____AKA						
____ALA						
____AMA						

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**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
____ANA						
____AOA						
____APA						
____AQA						

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner Initial  
AL ARA Diefendorff, Keith. "PC Processor Microarchitecture," *Microprocessor Report*, Vol. 13, No. 9; via Internet at <http://www.chipanalyst.com/x86/microarchitecture/index.html>, pp. 1-16.  
AL ASA Brehob et al. "Beyond RISC-The Post-RISC Architecture," *IEEE Micro*, March 1996, via Internet at <http://www.cs.msu.edu/~crs/cps920/>, pp. 1-19.  
AL ATA Gerritsen, Armin. "Out of Order Execution," via Internet at <http://cpusite.examedia.nl/docs/ooo.html>, pp.1-8.  
AL AUA Silc et al. "Asynchrony in Parallel Computing: From Dataflow to Multithreading," *Parallel and Distributed Computing Practices*, Vol. 1, No. 1, March 1998; via Internet at <http://www-csd.ijs.si/silc/pdcp.html>, pp.1-39.

Examiner:

Sumee J. Li

Date Considered:

3/4/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.